FIG. 4A

functionA:

402 1v.p v0, 0, 2 !load VLIW 0 with the next 2 instructions

instr1
instr2 404

412 1v.p v0, 1, 3 !load VLIW 1 with the next 3 instructions

instr3
instr4
instr5 414
instr5 420 xv.p v0, 0, e = AM !execute VLIW 0, enabling units A and M

422 xv.p v0, 1, e = AMS !execute VLIW 1, enabling units A, M and S

ret

FIG. 4B

450

functionA':

FIG. 5

- 500

510-0: Program start

511-1:loop 10 times 512- executive VLIW a

if condition then 513-

executive VLIW b 514-2:

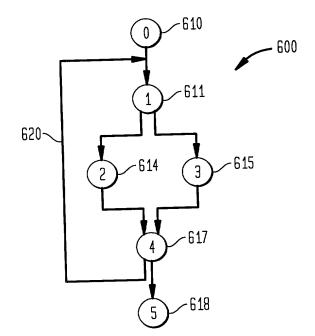
515-3: 516else

execute VLIW c end if

517-4:end loop

518-5: Program end

FIG. 6



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FIG. 7

0 1-710
700
10-711
2 7-714 3 3-715

FIG. 8

FIG. 9

1 0 LVa-822
800
912 a
900

824-LVb 2 7
3 LVc-826
814-XVb 2 7
3 XVc-816